

Sphinx - Signal and Power Integrity Co-Simulator

for Signoff

We knew we had something special...but when Customers evaluated Sphinx, here is what they said:

"This is the first time we have seen accurate correlation in both frequency and time domains"

"First time we have seen a tool show accurate results at both high and low frequencies"

Sphinx customers are seeing a new dawn in Signal and Power Integrity analysis. Based on the revolutionary new Multi-Layer Finite Difference Method (M-FDM), Sphinx looks at all layer coupling effects and accounts for many phenomenon not accounted for in other EM tools. This provides superior accuracy while improving simulation turnaround time. Does not require integrating SPICE or IBIS models or simulation waveforms to analyze a design.

Benefits to users:

The M-FDM methodology is very accurate and efficient, which allows for faster simulation results and more complex design analysis. User is not required to decide between accuracy or complexity; they can have both in *"Sphinx for Signoff"*

Correctly accounts for fringe and gap fields, aperture coupling, wrap around currents, and return path discontinuities other tools do not

"What if" analysis is supported by allowing users to modify their existing designs by adding layers, transmission lines, vias and capacitors. Significantly reducing analysis and design verification times

Scripting console allows non-graphics operations to be performed in batch mode requiring less memory and significantly less time to complete

Users can import their design, perform edits, select mesh size and view everything prior to simulation allowing improved flexibility, accuracy, and transparency in our methods

Computes S, Y and Z Network Parameters during simulation allowing user to determine how they want to review results

An integrated and proven flow between Frequency and Time Domain analyses allowing users easy model creation and excellent correlation between both domains

Sphinx can easily be integrated with any existing CAD Tool flows by supporting MCM, BRD, SiP and DXF file formats

Support 32 and 64 bit Windows 7, Vista and XP enabling users to analyze large, complex designs

Sphinx for Signoff - Why not test drive today?

Available Today – contact sales@e-systemdesign.com

Patent granted and others pending

