

## E-System Design, Inc. includes Sphinx DC in their Advanced Analysis Kits Starting at \$495.00

DC and frequency analysis of your IC package and printed circuit board designs just got a whole lot easier and much more affordable

February 1, 2011: DesignCon booth #751

E-System Design, Inc. today announced the release of *Sphinx DC* a DC analysis simulator, to complement its *Sphinx* family of Signal and Power Integrity IC package and printed circuit board simulators. Designers wanting to evaluate *Sphinx DC* can do so very quickly and easily. *Sphinx DC* has been added to E-System Design's Advanced Analysis Kit offering.

By adding *Sphinx DC* to their design flows, users will gain the following benefits:

- Ensure suitable DC levels available at both voltage and ground terminals of chip for correct operation
- Ensure DC voltage levels that do not cause premature reliability issues
- Allow DC system level simulation by providing a condensed Spice circuit
- Identify areas causing unexpected Joule heating effects
- Identify signal voltage drops that cause receiver latching failures
- Avoid costly migration decisions when considering migrating to another process or architecture

"*Sphinx*", "*Sphinx for Signoff*" and "*Sphinx DC*" are revolutionary new Signal and Power Integrity Simulators for IC packaging and printed circuit board design. The *Sphinx* product family uses a unique methodology (M-FDM) that is both extremely accurate as well as very efficient. Since its introduction 2+ years ago, numerous customers have *validated* *Sphinx's* accuracy and simulation run times. Many of these evaluations were performed on advanced and complex packages and printed circuit boards that other commercially available tools *could not* simulate accurately. The simple yet powerful GUI in these products is very easy to learn, easy to use, and is compatible with industry standard tool flows.

"Users that have already purchased the Advanced Analysis Kits have found immediate value in learning advanced signal and power integrity methods while applying these methods using *Sphinx* on their designs." said Gene Jakubowski, CEO and co-founder of E-System Design. "By adding *Sphinx DC*, the user experience will be enhanced; increasing their analysis, confidence, and significantly decreasing the time needed to release a design for manufacturing. In addition, our purchasing models provide flexibility in how a *Sphinx* platform can be acquired for anyone considering an upgrade to their current methodology."

The upgraded Advanced Analysis Kits are available now and include:

- A 2+ hour video seminar on "Designing for Signal and Power Integrity" presented by our CTO and Georgia Institute of Technology Distinguished Professor, Madhavan Swaminathan, covering the basics of SI/PI, interaction between signal and power, identifying return path discontinuities, advanced concepts and a reliable methodology for reaching fast and accurate design closure.
- A copy of the book: "Power Integrity Modeling and Design for Semiconductors and Systems" –Prentice Hall, by principal author Madhavan Swaminathan
- Applications videos to help resolve problems commonly found in today's designs along with training videos on how to use *Sphinx* to help reach design closure
- A 30 day evaluation license of *Sphinx* or *Sphinx for Signoff* AND *Sphinx DC*

The pricing for the Advanced Analysis Kits is \$495.00 for *Sphinx/Sphinx DC* and \$995.00 for *Sphinx for Signoff/Sphinx DC*, and can be ordered directly from the E-System Design website at [www.e-systemdesign.com](http://www.e-systemdesign.com)

Additional features of *Sphinx*, *Sphinx for Signoff* and *Sphinx DC* include:

- Support for DXF, MCM, BRD and SiP file formats
- Perform quick 'what if analysis' by allowing users to modify their existing designs inside of the *Sphinx* platform (Can add or delete, layers, transmission lines, vias, and capacitors)
- Add vendors' capacitor libraries to ensure only approved capacitors are used in any 'what if' analysis
- View meshed design before simulation, removing any doubt whatsoever of 'what' is being analyzed

- Easily perform Time Domain analysis with an integrated and proven flow using Sphinx's Touchstone files that are converted into robust, passivity and causality conforming Spice netlists using IdEM Plus
- Incorporate spice models of complete packages and PCBs from Idem Plus for system level analysis
- Perform system level DC power distribution network analysis with a condensed DC Spice netlist (*Sphinx DC*)

## Availability

*Sphinx*, *Sphinx for Signoff* and *Sphinx DC* are available now from E-System Design. For more information, please visit our website: [www.e-systemdesign.com](http://www.e-systemdesign.com) or contact: [sales@e-systemdesign.com](mailto:sales@e-systemdesign.com).

## About E-System Design

**E-System Design, Inc.** is an industry leading provider of electronic design automation (EDA) tools for advanced electronic systems. Our focus is on System Integrity through the accurate modeling of highly integrated IC packages, printed circuit boards and systems, so our customers can achieve optimal performance and first pass manufacturing success. The company is headquartered in the metro Atlanta, GA area, with direct and third party sales and support services available throughout the world to better serve the electronics industry. Additional information about E-System Design, our products, and our technology is available at [www.e-systemdesign.com](http://www.e-systemdesign.com).

Editorial Contact:

Gene Jakubowski  
E-System Design, Inc.  
678-296-3772  
[ski@e-systemdesign.com](mailto:ski@e-systemdesign.com)

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